100

	Y ADDRESS BITS Ai .... A_4 A_3 A_2 A_1 A_0	WORD#	ADDR. FIELD
FIRST GROUP	0 0 0 0 0 0	0	I
	0 0 0 0 0 1	1	
	0 0 0 0 1 0	2	
	0 0 0 0 1 1	3	
	0 0 0 1 0 0	4	II
	0 0 0 1 0 1	5	
	0 0 0 1 1 0	6	
	0 0 0 1 1 1	7	
SECOND GROUP	0 0 1 0 0 0	8	III
	0 0 1 0 0 1	9	
	0 0 1 0 1 0	10	
	0 0 1 0 1 1	11	
THIRD GROUP	0 0 1 1 0 0	12	IV
	0 0 1 1 0 1	13	
	0 1 1 1 1 0	14	
	0 1 1 1 1 1	15	
		127	

Fig. 1

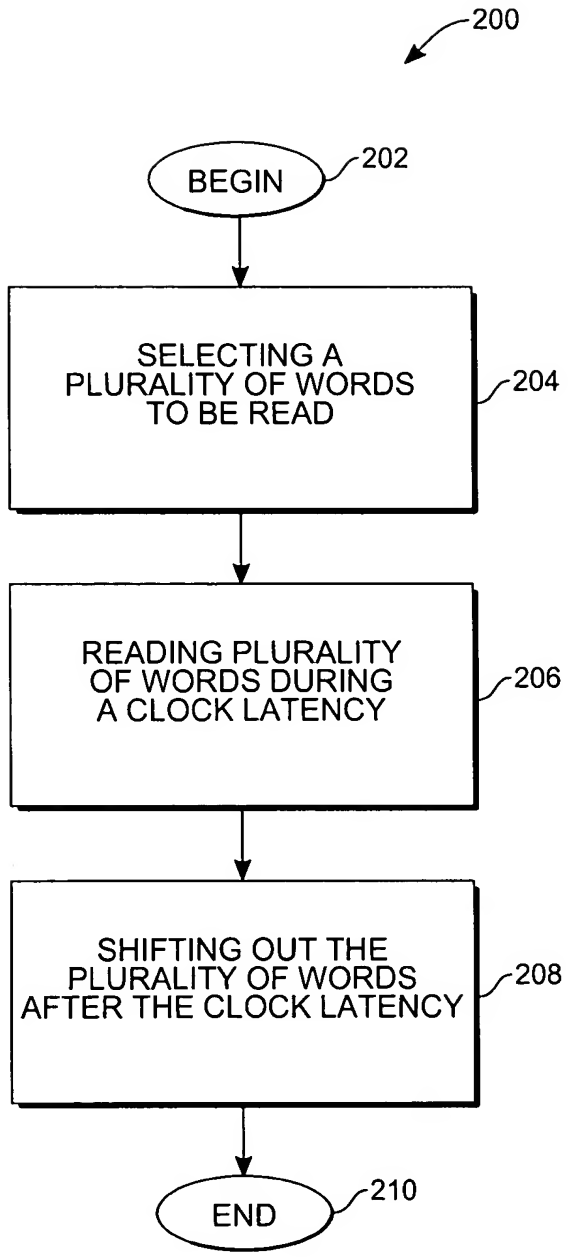
200
BEGIN
202
SELECTING A PLURALITY OF WORDS TO BE READ
204
READING PLURALITY OF WORDS DURING A CLOCK LATENCY
206
SHIFTING OUT THE PLURALITY OF WORDS AFTER THE CLOCK LATENCY
208
END
210

Fig. 2

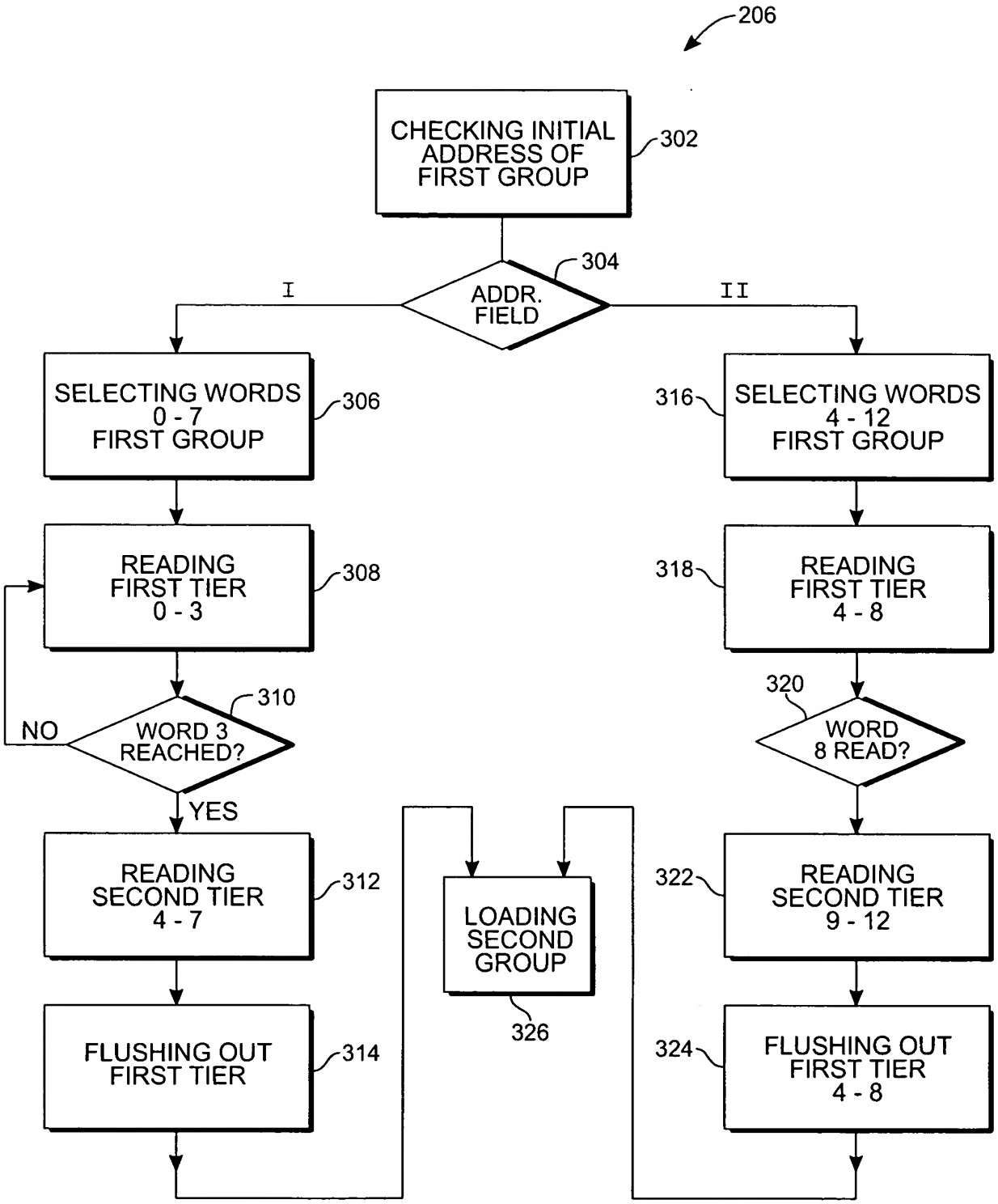
206
CHECKING INITIAL ADDRESS OF FIRST GROUP
302
304
ADDR. FIELD
I
II
SELECTING WORDS 0 - 7 FIRST GROUP
306
SELECTING WORDS 4 - 12 FIRST GROUP
316
READING FIRST TIER 0 - 3
308
READING FIRST TIER 4 - 8
318
310
WORD 3 REACHED?
NO
YES
320
WORD 8 READ?
READING SECOND TIER 4 - 7
312
LOADING SECOND GROUP
326
READING SECOND TIER 9 - 12
322
FLUSHING OUT FIRST TIER
314
FLUSHING OUT FIRST TIER 4 - 8
324

Fig. 3

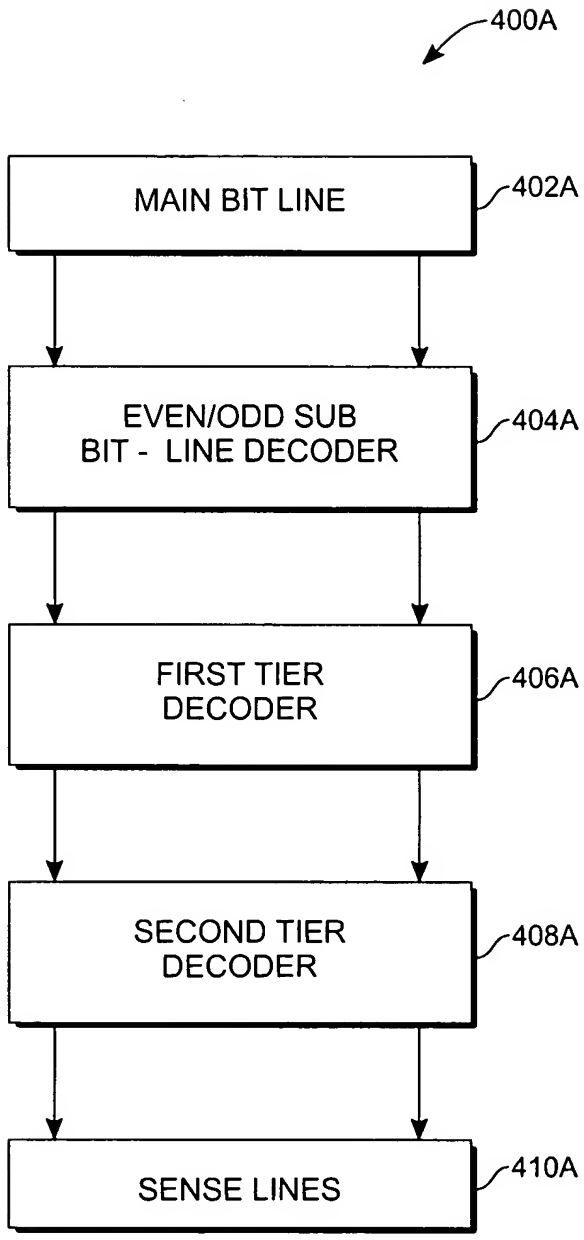
400A
MAIN BIT LINE
402A
EVEN/ODD SUB
BIT - LINE DECODER
404A
FIRST TIER
DECODER
406A
SECOND TIER
DECODER
408A
SENSE LINES
410A

Fig. 4A

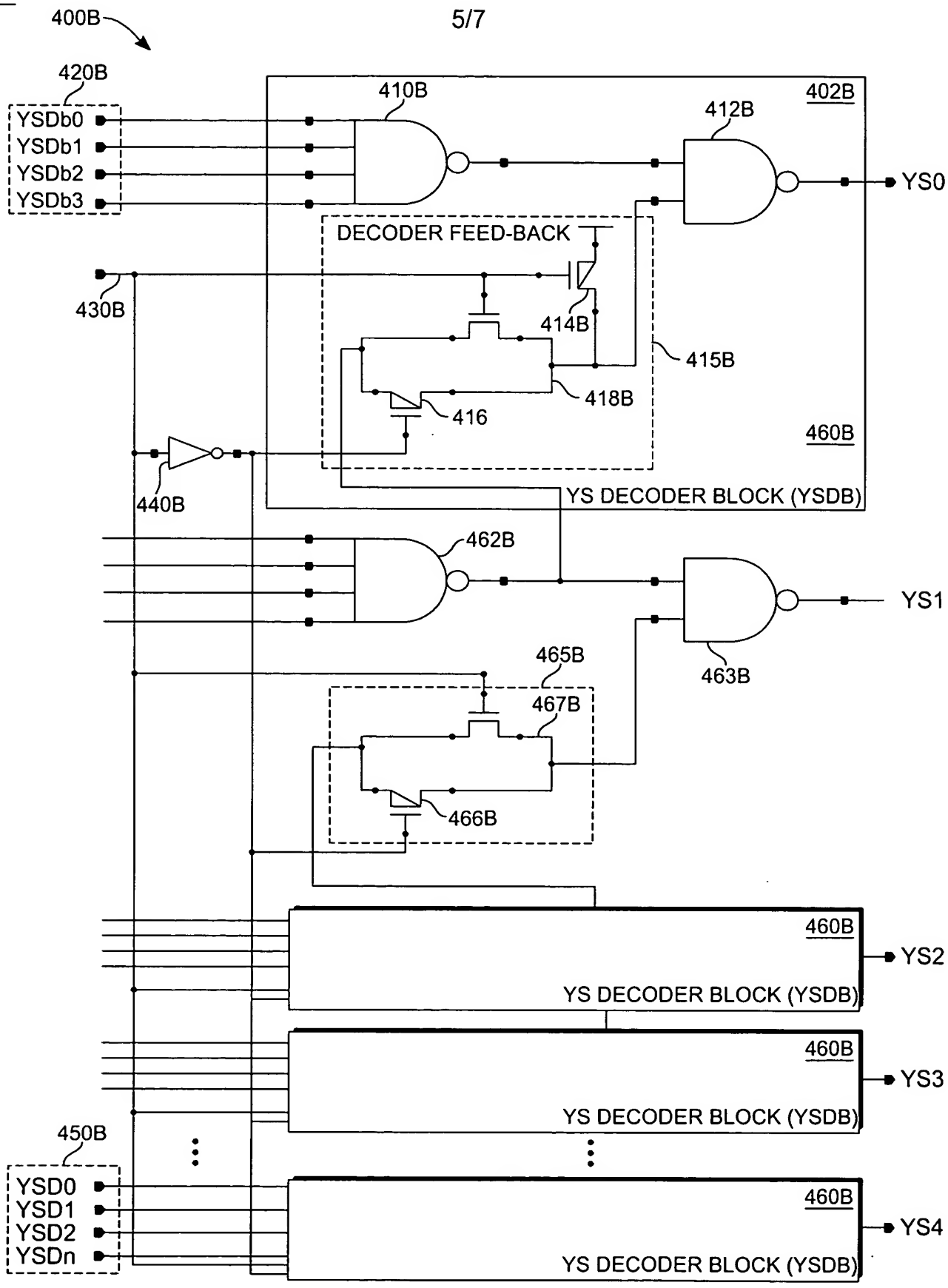
400B
420B
YSDb0
YSDb1
YSDb2
YSDb3
410B
412B
402B
YS0
DECODER FEED-BACK
430B
414B
415B
418B
416
460B
440B
YS DECODER BLOCK (YSDB)
462B
YS1
463B
465B
467B
466B
460B
YS2
YS DECODER BLOCK (YSDB)
460B
YS3
YS DECODER BLOCK (YSDB)
450B
YSD0
YSD1
YSD2
YSDn
460B
YS4
YS DECODER BLOCK (YSDB)

Fig. 4

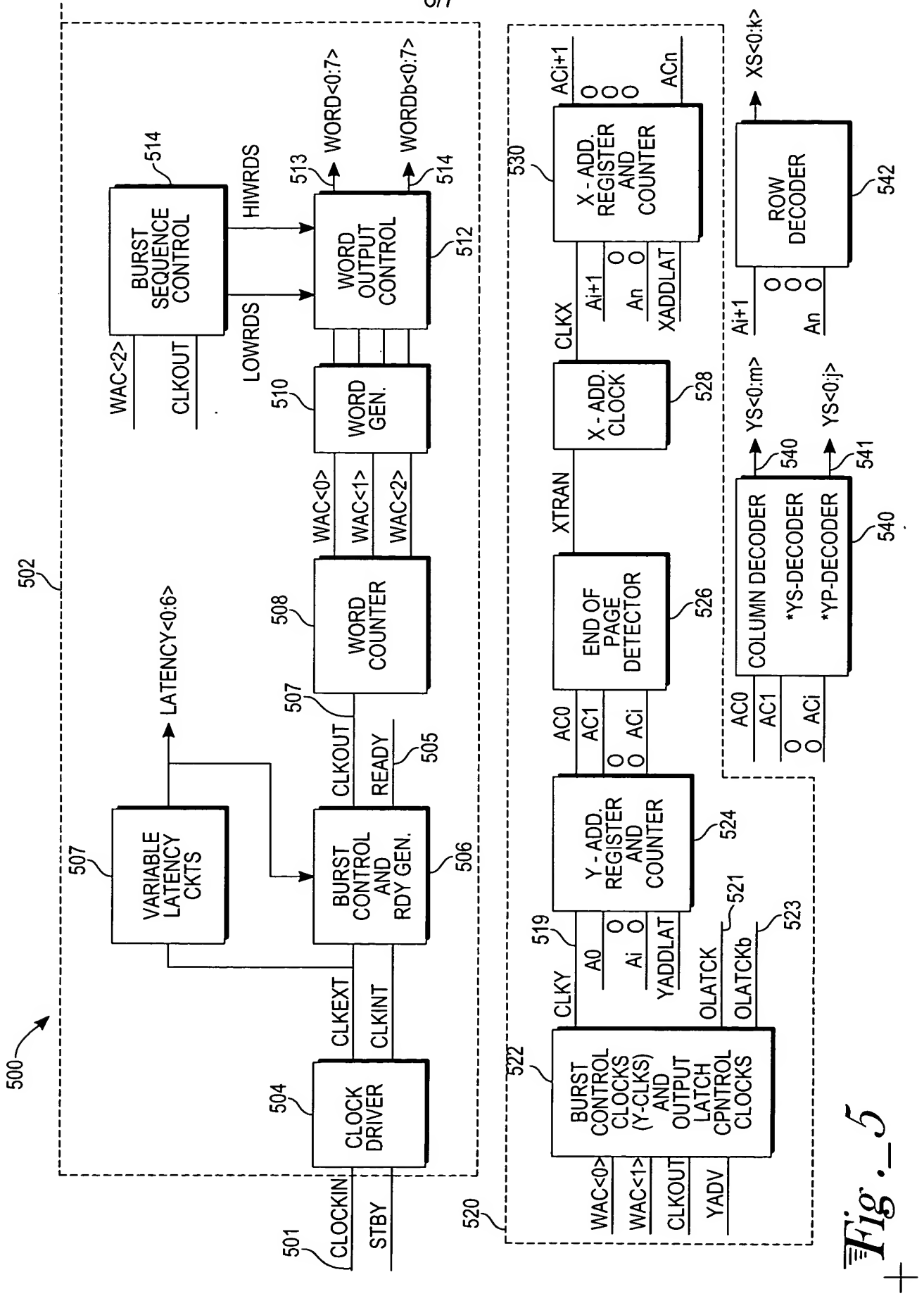
500
502
501
CLOCKIN
STBY
504
CLOCK DRIVER
CLKEXT
CLKINT
507
VARIABLE LATENCY CKTS
LATENCY<0:6>
506
BURST CONTROL AND RDY GEN.
CLKOUT
READY
505
507
508
WORD COUNTER
WAC<0>
WAC<1>
WAC<2>
510
WORD GEN.
WAC<2>
CLKOUT
514
BURST SEQUENCE CONTROL
HIWRDS
LOWRDS
513
WORD<0:7>
514
WORDb<0:7>
512
WORD OUTPUT CONTROL
520
522
BURST CONTROL CLOCKS (Y-CLKS) AND OUTPUT LATCH CPNTROL CLOCKS
WAC<0>
WAC<1>
CLKOUT
YADV
CLKY
519
A0
Ai
YADDLAT
524
Y - ADD. REGISTER AND COUNTER
OLATCK
521
OLATCKb
523
AC0
AC1
ACi
526
END OF PAGE DETECTOR
XTRAN
528
X - ADD. CLOCK
CLKX
530
Ai+1
An
XADDLAT
ACi+1
ACn
X - ADD. REGISTER AND COUNTER
542
ROW DECODER
XS<0:k>
Ai+1
An
AC0
AC1
ACi
540
COLUMN DECODER
*YS-DECODER
*YP-DECODER
YS<0:m>
YS<0:j>
541

Fig. 5

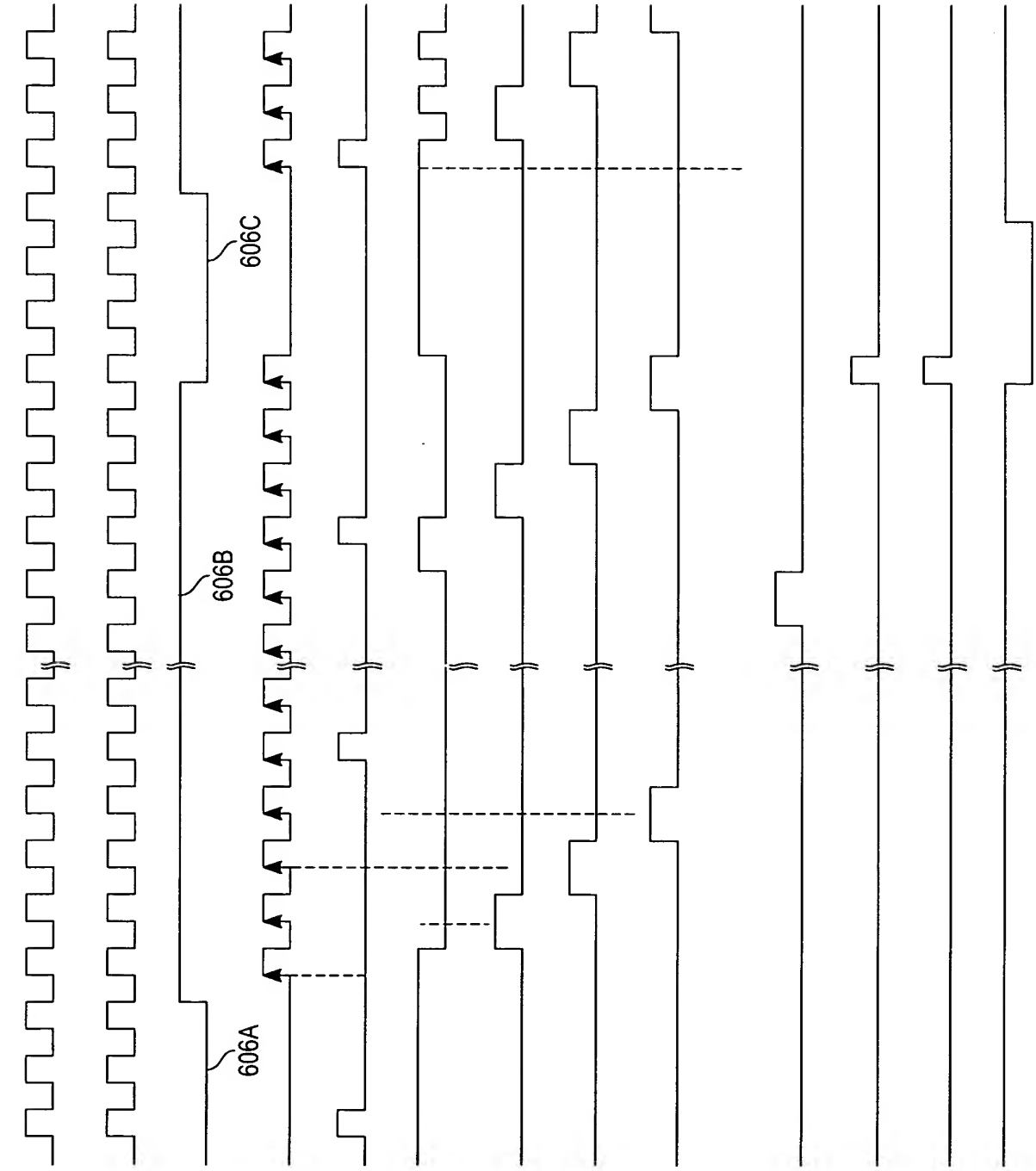
602 CLKIN
604 CLKINT
606 LATENCY3
606A
606B
606C
608 CLKOUT
610 CLKY
612 WORD0
614 WORD1
616 WORD2
618 WORD3
620 WORD7
622 CLKX
624 XTRAN
626 RDY

Fig. 6